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16DEC03 E859562-3 D00056
P01/7700 0.00-0329014.5 NONE

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Request for grant of a patent

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The Patent Office

Cardiff Road
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NP9 1RH

1. Your reference WJN/P36250GB

2. Patent application number *(The Patent Office will fill in this part)* 0329014.5

3. Full name, address and postcode of the or of each applicant *(underline all surnames)*

Cambridge University Technical Services Limited
The Old Schools
Cambridge
Cambridgeshire CB2 1TS

Patents ADP number *(if you know it)*

If the applicant is a corporate body, give the country/state of its incorporation 06956809004

4. Title of the invention

Hologram Viewing Device

5. Name of your agent *(if you have one)*

"Address for service" in the United Kingdom to which all correspondence should be sent *(including the postcode)*

Kilburn & Strode
20 Red Lion Street
London
WC1R 4PJ

Patents ADP number *(if you know it)* 125001 ✓

6. If you are declaring priority from one or more earlier patent applications, give the country and the date of filing of the or of each of these earlier applications and *(if you know it)* the or each application number

Country	Priority application number *(if you know it)*	Date of filing *(day / month / year)*

7. If this application is divided or otherwise derived from an earlier UK application, give the number and the filing date of the earlier application

Number of earlier application	Date of filing *(day / month / year)*

8. Is a statement of inventorship and of right to grant of a patent required in support of this request? *(Answer 'Yes' if:*

a) any applicant named in part 3 is not an inventor, or
b) there is an inventor who is not named as an applicant, or
c) any named applicant is a corporate body.
See note (d))

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Patents Form 1/77

9. Enter the number of sheets for any of the following items you are filing with this form. Do not count copies of the same document

Continuation sheets of this form	
Description	7
Claim(s)	2 DL
Abstract	
Drawing(s)	5+5

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Priority documents	
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Statement of inventorship and right to grant of a patent *(Patents Form 7/77)*	
Request for preliminary examination and search *(Patents Form 9/77)*	
Request for substantive examination *(Patents Form 10/77)*	
Any other documents *(please specify)*	

11. I/We request the grant of a patent on the basis of this application.

Signature Kilburn - Strode Date 12.12.2003

12. Name and daytime telephone number of person to contact in the United Kingdom

William J Neobard
Tel: 020 7539 4200

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Viewing angle enhancement for holographic displays

The present invention relates to a holographic display and a method of increasing the viewing angle of a hologram on a pixellated hologram display device

A well known problem with holographic displays is that of viewing angle, which is particularly relevant to 3D information display. Wide viewing angle requires very small pixels in the display, all of which must be individually addressable, resulting in difficulties in manufacture and in the bandwidth required to drive such a device.

Where binary phase holographic systems (both 2D and 3D) are used a further problem is the presence of a conjugate image in the replay field (see Figure 5 herein).

It is an object of embodiments of the present invention to address one or both of these issues.

According to one aspect of the invention there is provided a holographic display comprising a pixellated hologram display device having a predetermined resolution and a pixellated phase mask arranged such that holograms displayed on the SLM are viewed through the phase mask, wherein the phase mask has a resolution higher than the predetermined resolution.

In an embodiment, the display comprises a pixellated hologram display device having a predetermined resolution and a pixellated phase mask arranged such that holograms displayed on the SLM are viewed through the phase mask, wherein the phase mask co-operates with the SLM such that the repeating

pattern of holographic elements has a higher resolution than the predetermined resolution.

The hologram display device may be arranged to display binary phase holograms and the phase mask have four phase levels

The display may be constructed and arranged to operate at a given optical wavelength, and taking one of the phase levels as a reference, the others provide respective phase shifts of $\pi/2$, π and $3\pi/2$ at the given wavelength.

The hologram display device may be arranged to display four phase holograms and the phase mask has two phase levels.

The hologram display device may comprises an SLM.

In a second aspect the invention relates to a method of increasing the viewing angle of a hologram on a pixellated hologram display device having a predetermined resolution, the method comprising disposing a pixellated phase mask with respect to the pixellated hologram display device for viewing the hologram, wherein the resolution of the pixellated phase mask is greater than that of the pixellated hologram display device.

In a further aspect the invention relates to a method of viewing a pixellated hologram, the pixels of the hologram having a predetermined resolution, comprising viewing the hologram through a pixellated phase mask, wherein the resolution of the pixellated phase mask is greater than that of the pixellated hologram.

Exemplary embodiments of the invention will new be described by way of example only with respect to the attached drawings in which:

Figure 1 shows a partial cross-sectional view through a holographic display device embodying the invention;

Figure 2 shows a top elevation of the holographic display device of Figure 1;
Figure 3 shows comparative results of two test simulations demonstrating that by use of the invention viewing angle can be increased;

Figure 4 shows the results of an embodiment of the invention using a binary phase SLM; and

Figure 5 shows the artefact of conjugate image of a binary phase SLM.

Referring now to Figure 2, an embodiment of the invention which uses a ferro-electric liquid crystal spatial light modulator (100) as its display device will now be described. It will be understood that other binary phase SLMs can be substituted for a FELC SLM. Moreover the use of a binary phase SLM is not fundamental to the invention in its broadest aspect, as the viewing angle of SLMs in general may be increased by use of the invention

Typical SLMs that may be used include ferroelectric SLMs, nematic SLMs and OASLMs (optically-addressed spatial light modulators), and those using electroclinic, pi-cells, flexoelectric, antiferroelectric, ferrielectric, V-shaped switching cells, and guest-host dye cells. Non-liquid crystal technologies such as OLED displays, vacuum fluorescent displays, electroluminescent displays, MEMS devices such as DMDs, are also applicable.

As seen in Figure 2, the SLM has plural image pixels (101-103) of which three are shown. As seen in Figure 3, the pixels (101,103,111) are arranged in a regular 2-D array. An actual SLM has pixels amounting to several hundred pixels square. Rectangular arrays and arrays of other shapes may also be used. Circuitry for addressing the pixels, and other layers such as electrode layers will be present but are not shown for ease of explanation. It will be understood that in the present embodiment the liquid crystal material is continuous across the part of the SLM shown, and that the designation of pixels corresponds to electrode arrangements to which field may be applied to cause the relevant volume of LC material to adopt a different orientation to the adjacent such volume.

A four-level phase mask (120) with greater resolution than that of the base hologram, of which the greatest resolution corresponds to the underlying, is disposed over and above the SLM (100). The phase mask is typically of plastic, and is random in that each area of the mask has an equal probability of each of the four levels. The mask may be physically on the SLM or disposed above it.

As most clearly seen in Figure 1, in the present embodiment, there are four mask areas (121,122,131,132) or "pixels" to each pixel (101) of the SLM.

Returning to Figure 2, which shows three adjacent and contiguous SLM pixels (101-103) and hence six adjacent and contiguous phase mask pixels (121-126), the first pixel (121) has a level or thickness selected to be the base thickness for the wavelength of concern and for the material of the phase mask which here is homogeneous. The second pixel (122), which is adjacent the first pixel (121), has a thickness greater than the thickness of the first pixel (121) by an amount to give an additional $\pi/2$ phase change for the wavelength of concern. The third pixel (123), which is adjacent the second pixel (122), again has the base

thickness. The fourth pixel (124), which is adjacent the third pixel (123), has a thickness greater than that of the base thickness by an amount giving in use a π phase change to the wavelength of concern; the fifth pixel (125), which is adjacent the fourth pixel (124), has a thickness greater than that of the base thickness by an amount giving in use a $3\pi/2$ phase change to the wavelength of concern. The sixth pixel (106), which is adjacent the fifth pixel (125), has the base thickness.

It will be understood that the phase mask could also be of constant thickness with each pixel phase level being achieved by different refractive indices. This may be achieved by differentially subjecting a photosensitive material in the respective phase level regions to different amounts of radiation.

It will also be understood that the underlying pixellated hologram need not be displayed on an SLM but could in fact be a permanent hologram

It has been shown that this new type of phase mask allows viewing angle to be increased by several times, providing at the same time an increase in effective resolution without any increase in required bandwidth: a phase mask of half the pitch of the hologram can approximately double the viewing angle and quadruple the number of addressable points in the replay field. These gains are however at the expense of additional noise in the replay field.

To overcome this defect reference is made to our co-pending patent application no XXXXXX (agents ref P36148GB.) in which we describe a technique which we call OSPR which allows a very rapid calculation of a hologram for a scene. The patent application describes how perceived noise in a hologram display is reduced by displaying several independently generated sequential holograms

per frame. This technique may be applied to the present noise increase to allow a high quality low perceived noise display, having a wide viewing angle.

A known holographic display, believed to be the most advanced holographic display currently in existence, exhibits a conjugate image restricting the usable display area . In addition, the small viewing angle is a significant limitation for many potential applications. Application of an embodiment of the invention, using a super-resolution four-level phase mask technique to the known display would significantly widen its commercial viability (see Figure 4).

A test simulation was performed of a 3D display of resolution 1024 x 1024 with 10 μm pixels (ordinarily giving a viewing angle of 3 degrees) employing a 4096 x 4096 phase mask with 2.5 μm pixels, fabricated on polycarbonate, giving an enhanced viewing angle of around 14 degrees. A single hologram was calculated using OSPR (N = 1) to display a 3D wireframe cube on the display.

In Figure 3, a close-up view of the corner of the cube is shown, generated from a 1024 x 1024 hologram with 4096 x 4096 phase mask (left) and a 4096 x 4096 hologram with 4096 x 4096 phase mask (right). As can be seen, the left image is of comparable quality to the right, despite the 16-fold reduction in hologram bandwidth.

Although the embodiments described relate to a super-resolution phase mask, a same-resolution phase mask shifted by a half pixel in both directions, so that the pixel intersection of the phase mask is central or roughly central of the underlying hologram can also be used to improve viewing angle.

An embodiment of the invention has now been described. The invention however is not to be taken as limited to the features disclosed but instead extends to the full scope of the appended claims.

CLAIMS

1. A holographic display comprising a pixellated hologram display device having a predetermined resolution and a pixellated phase mask arranged such that holograms displayed on the SLM are viewed through the phase mask, wherein the phase mask has a resolution higher than the predetermined resolution.

2. A holographic display comprising a pixellated hologram display device having a predetermined resolution and a pixellated phase mask arranged such that holograms displayed on the SLM are viewed through the phase mask, wherein the phase mask co-operates with the SLM such that the repeating pattern of holographic elements has a higher resolution than the predetermined resolution.

3. A holographic display according to claim 1 or 2, wherein the hologram display device is arranged to display binary phase holograms and the phase mask has four phase levels

4. A holographic display according to claim 3, wherein the display is constructed and arranged to operate at a given optical wavelength, and taking one of the phase levels as a reference, the others provide respective phase shifts of $\pi/2$, π and $3\pi/2$ at the given wavelength.

5. A holographic display according to claim 1 or 2, wherein the hologram display device is arranged to display four phase holograms and the phase mask has two phase-levels.

6. A holographic display according to any preceding claim wherein the hologram display device comprises an SLM.

7. A method of increasing the viewing angle of a hologram on a pixellated hologram display device having a predetermined resolution, the method comprising disposing a pixellated phase mask with respect to the pixellated hologram display device for viewing the hologram, wherein the resolution of the pixellated phase mask is greater than that of the pixellated hologram display device.

8. A method of viewing a pixellated hologram, the pixels of the hologram having a predetermined resolution, comprising viewing the hologram through a pixellated phase mask, wherein the resolution of the pixellated phase mask is greater than that of the pixellated hologram.

9. A holographic display comprising a pixellated hologram display device having a predetermined resolution and a pixellated phase mask arranged such that holograms displayed on the SLM are viewed through the phase mask, wherein the phase mask is arranged so that respective locations where its pixels meet are disposed above generally central regions of the pixels of the display device.

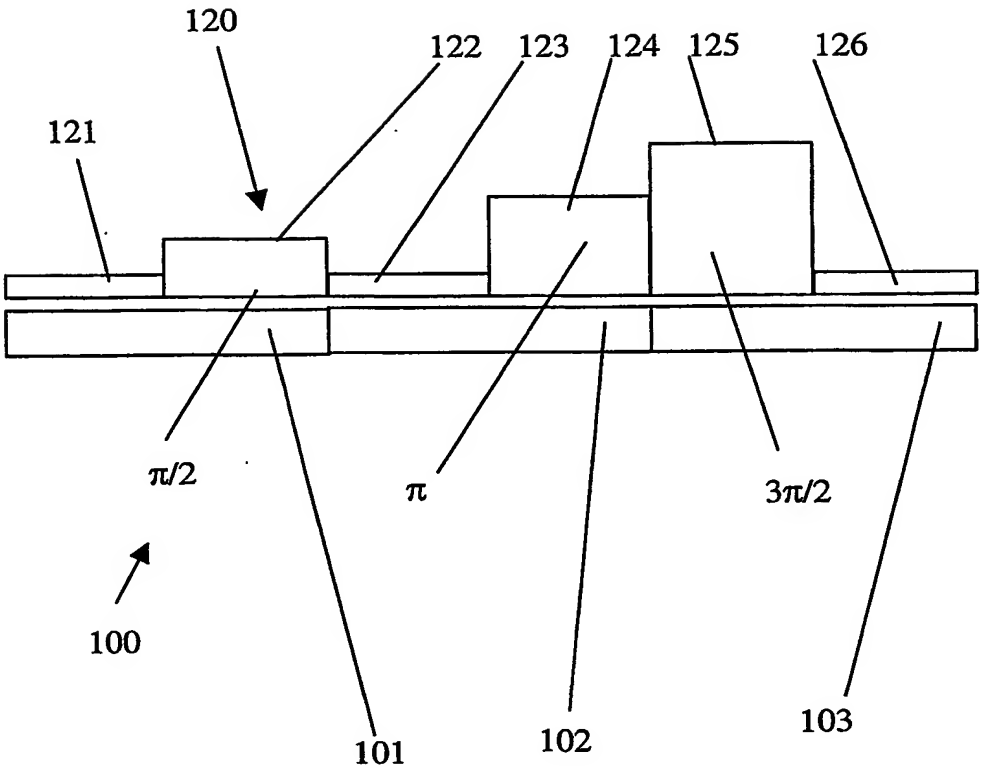

Figure 1

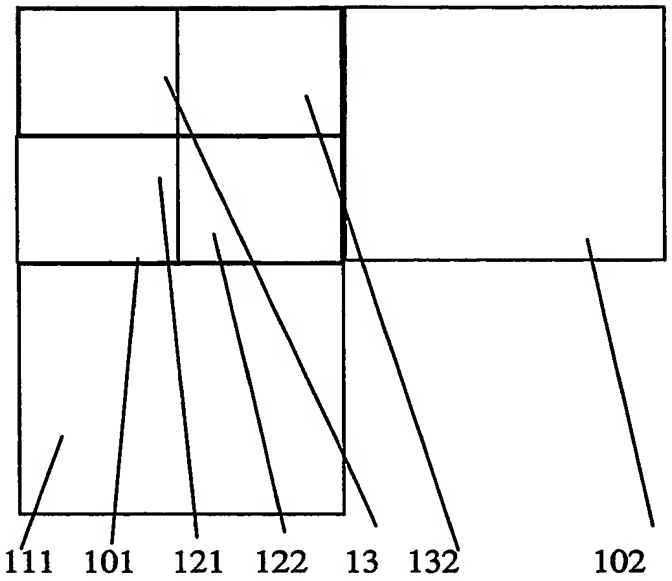

Figure 2

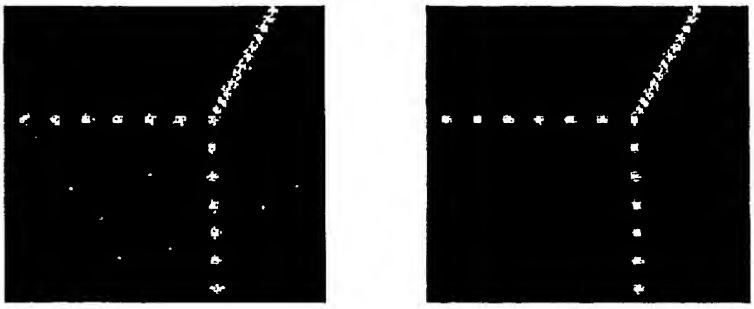

Figure 3

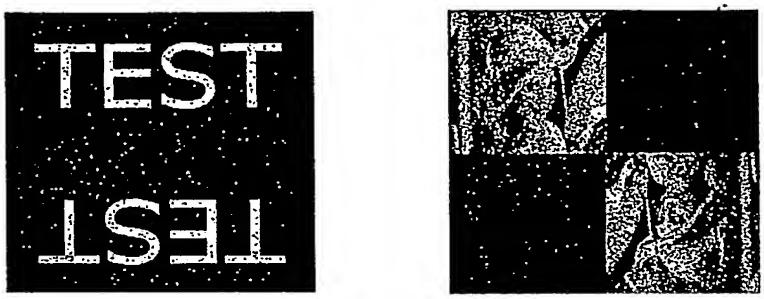

Figure 5

Document made available under the Patent Cooperation Treaty (PCT)

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